

DOLPHIN

DESIGN

SLED 3.4.1
New Features
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Dolphin Design SAS

1bisA Chemin du Pré Carré - 38240-MEYLAN - FRANCE

Phone: + 33 476 411 096 - Fax: + 33 476 902 965

www.dolphin-design.fr - contact@dolphin.fr

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THANKS

As always for new releases, we would like to thank those customers who take the time to report problems and/or to suggest improvements (please remember that the best way to do so is by sending an email to support@dolphin.fr or contact@dolphin.fr with an accurate description of your problem or suggestion, together with the relevant files if any). As you will see in the new features, we do our best to take remarks into account. And even if your suggestion does not appear this time, don't think it was lost or disregarded. Simply, it means that its implementation could not fit into the development plan for this particular release, but be assured that we will try to take it into account in a future release.

WEB SITE

Our web site www.dolphin-design.fr is a source of information on our EDA solutions. Aside from evaluation kits for our products, a number of application notes, courses or upgrades are available for download.

SLED

SLED is a hierarchical schematic entry solution of the third generation which delivers the long awaited dual capability for Graphic Entry and Scriptability at once. It blends efficiently the feasibility of linking components and of writing scripts for configuring a netlist hierarchically. Interoperability with other schematic entry tools is ensured for capitalizing on legacy designs and cooperative work, and interoperability in the Design Chains is ensured through standard design exchange formats and scriptability for customization by CAD managers.

PSL

Relevant options of SMASH include native support for simulation of PSL¹ properties, both assertions and coverage, with very low time and memory overhead.

The integration of PSL is complete with source code syntax coloring, association of verification units with Verilog or VHDL models or instances, logging of PSL assertion violations, reporting of PSL sequence coverage results, and breaking into the source level debugger for investigation of design defects.

Assertion-Based Verification

The SLED SDG² option enables conversion of PSL assertions into synthesizable RTL models. This makes it possible for the designer to automatically integrate PSL verification units into a Design Under Test in an FPGA for emulation or in a testchip. Embedding hardware verification units in prototypes increases verification speed by several orders of magnitude.

Automated generation of synthesizable models from PSL assertions can also be used as an efficient alternative to writing safety related parts of a design directly in RTL. These hardware verification units are integrated for embedded monitoring.

¹Property Specification Language

²Synthesizable Detector Generator

SUPPORTED PLATFORMS

Microsoft Windows

SLED is designed to run on Microsoft Windows Vista / 7 / 8 / 10 on x86_64 platforms.

Linux on Intel x64 platform

SLED is designed to run under X-Window on RedHat Enterprise Linux 6 (RHEL6) and supports compatible Linux distributions on x86_64 platforms.

CREDITS & COPYRIGHTS

Qt : A C++ framework for cross-platform programming

<http://qt.digia.com>

Qt Development Frameworks creates application development platforms for desktop and mobile device innovation.

Qt Development Frameworks Oyj, Valimotie 21, 00380 Helsinki Finland +358 10 313 3000 © 2012 Digia. Legal and Privacy

Scintilla Source Code Editor Component

License for Scintilla and SciTE

Copyright 1998-2005 by Neil Hodgson <neilh@scintilla.org>

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LIBJSON Component

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SLED

SLED

Enhancements

- Implemented the ability to lock a library in a read-only mode (DDIsa04236 - SLED 3.4.0)
- Implemented the ability to print a HTML page (DDIsa13570 - SLED 3.4.1)

Bug fixing

- Updated the 'Credits' dialog by adding copyright of Tcl Library Source Code (DDIsa13464 - SLED 3.4.0)
- Corrected the delete command on document (File/Url) when its belongs to the project (DDIsa13469 - SLED 3.4.0)
- Corrected the handling of the parameters in Property Editor on a multiple selection (DDIsa13477 - SLED 3.4.0)
- Corrected an application crash when starting SLED with no DISPLAY environment variable defined (DDIsa13482 - SLED 3.4.0)
- Corrected the Property Editor update when a selection is done in the Project Manager (DDIsa13492 - SLED 3.4.0)
- Corrected the read-only status of the parameter name in the 'Edit Parameter' dialog (DDIsa13523 - SLED 3.4.0)
- Corrected SLED to work on Linux when the installation directory contains spaces (DDIsa13527 - SLED 3.4.0)
- Corrected the handling of the default style value in the Preferences dialog (DDIsa12909 - SLED 3.4.1)
- Corrected the loss of the wire style after a move or a a copy (DDIsa13533 - SLED 3.4.1)
- Corrected the document renaming after renaming the cell it belongs to (DDIsa13535 - SLED 3.4.1)
- Corrected the behavior of automatic compatibilities modification on locked libraries (DDIsa13548 - SLED 3.4.1)
- Corrected the management of parameter name case when several projects are loaded consecutively (DDIsa13553 - SLED 3.4.1)
- Corrected the management of port after connecting a net label on propagated instance pin when the net label has the same name as the propagaged net name of the propagated instance pin (DDIsa13554 - SLED 3.4.1)

- Corrected the management of port after removing an instance with propagated pin when a net label with the same name as the propagated net name exists in the schematic (DDIsa13555 - SLED 3.4.1)
- Corrected the management of port after renaming the propagated net name of an instance pin when a netlabel with the same name as the initial propagated net name exists in the schematic (DDIsa13563 - SLED 3.4.1)
- Corrected the wrong generation of port when a wire is connected to a propagated instance pin if a netlabel with the same name as the propagated net name exists in the schematic (DDIsa13567 - SLED 3.4.1)
- Corrected the decorate instance process when the instance has propagated pin (DDIsa13588 - SLED 3.4.1)
- Corrected the backannotation activation after renaming the .opsba file previously activated (DDIsa13589 - SLED 3.4.1)

SLED API

Enhancements

- Implemented means to define the library content type (Reference, Work) by using the new SLED API function SLED_AttributeSet (DDIsa13505 - SLED 3.4.0)
- Implemented means to attach SLED Hook to TCL procedure implemented in TCL file associated to Cell/Library (DDIsa13526 - SLED 3.4.1)

SLED DRC

Bug fixing

- Corrected the 'Unconnected input|output|bidirectional port' DRC rule for port generated from propagated hidden instance pin (DDIsa13552 - SLED 3.4.1)

SLED EXPORTER/IMPORTER

Bug fixing

- Corrected the export of instance of cell defined in the 'Tools' library (DDIsa13493 - SLED 3.4.0)
- Corrected the export of polygon defined by only two points (DDIsa13494 - SLED 3.4.0)

SLED NETLISTER

Bug fixing

- Corrected the evaluation of environment variable during the parameter evaluation (DDIsa13498 - SLED 3.4.0)